

SPECIFICATION

WOUND CHIP INDUCTORS

BRL2518 TYPE

TAIYO YUDEN

	Specifications	(1 / 1 1)
	BRL2518 TYPE	

1. Range of application

This specification sheet applies to small wound shielded chip inductor, BRL2518.

2. Ordering code

Example : $\frac{\text{BRL}}{(1)} \frac{2518}{(2)} \frac{\text{T}}{(3)} \frac{1\text{R}0}{(4)} \frac{\text{M}}{(5)}$

- (1) Type
- (2) External dimensions
- (3) Packing style (T: Taping)
- (4) Inductance
- (5) Inductance tolerance (M=±20 %)

3. Standard measuring method

Inductance : LCR meter (HP 4285A or equivalent)
Measuring signal level : 1V
Self-resonance frequency : Impedance/Material Analyzer (HP 4291A or equivalent)
DC resistance : DC Ohmmeter (HIOKI 3227 or equivalent)

Standard test conditions

Unless otherwise specified, temperature is 20±15 deg and the humidity is 65±20 %.

Should any doubt arise about the test results, further test shall be conducted at a temperature of 20±2 deg and a humidity of 65±5 %.

Inductance is in accordance with our standard measurement figures.

4. Operating temperature range

-25 deg to +105 deg (Containing self temperature increase)

5. Storage temperature range

-40 deg to +85 deg (Product without taping)

6. Electrical characteristics

Refer to table 1.

7. External dimensions and structural diagram

Refer to Table 2.

8. Environment test performance standards

Refer to Table 3.

9. Taping method

Refer to Table 4.

10. Packing form

Refer to Table 5.

11. Reflow profile chart

Refer to Table 6.

	Table 1	(2 / 1 1)
	ELECTRICAL CHARACTERISTICS	

Ordering Code	Nominal Inductance [μ H]	Inductance Tolerance [%]	D.C. Resistance $\pm 30\%$ [Ω]	Self Resonant Frequency min [MHz]	Rated*) Current max [mA]		Measuring Frequency [KHz]
					Saturation Current	Temperature Rise current	
BRL2518T1R0M	1.0	± 20	0.080	130	1600	1000	7.96
BRL2518T1R5M	1.5	± 20	0.100	100	1200	920	7.96
BRL2518T2R2M	2.2	± 20	0.135	80	1000	850	7.96
BRL2518T3R3M	3.3	± 20	0.300	70	800	580	7.96
BRL2518T4R7M	4.7	± 20	0.400	60	700	470	7.96

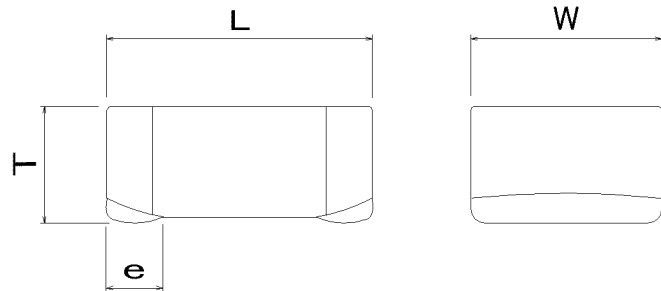
*) The saturation current value (Idc1) is the maximum DC current value having inductance decrease down to 30%. (at 20°C)

*) The temperature rise current value (Idc2) is the maximum DC current value having temperature increase up to 40°C. (at 20°C)

*) The rated current value is following either Idc1 or Idc2, which is the lower one.

	Table 2	(3 / 1 1)
	EXTERNAL DIMENSIONS AND STRUCTURAL DIAGRAM	

1. External dimensions

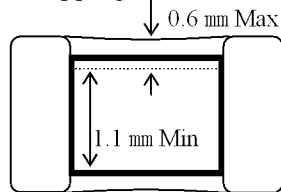


L	2.50±0.20
W	1.80±0.20
T	1.20 max
e	0.50±0.20

Unit: mm

※ External appearance

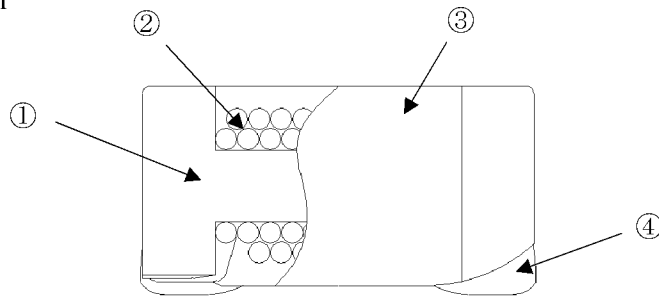
• Resin chipping



Set □ area as reference for flat level, over 70 % shall be flat.
Judgment shall be made according to the dimension below.
Insufficient resin on one side shall be 0.6 mm Max.
In case of insufficient resin on both side over 1.1 mm of flat surface shall be ensured.
(Template R part is not included on reference area.)

- The pinhole with ϕ up to 0.5 mm within specified area shall be regarded as non-defective.
Resin crack or pinhole which locates outside of specified area or which has contact with frame, shall be also regarded as non-defective.

2. Structural diagram



1. Ferrite core
2. Coil material
3. Over-coating resin
4. Electrode

Ni—Zn ferrite
Polyurethane-copper wire
Epoxy resin, containing ferrite powder
Base material : Ag
Foundation plating : Ni
Surface plating : Sn

	Table 3	(4 / 11)
	STANDARDS	

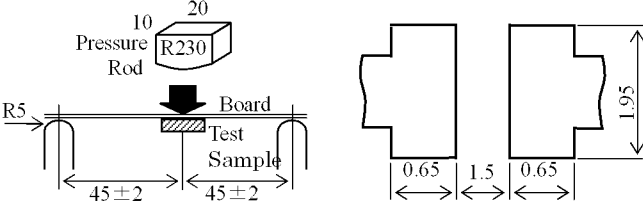
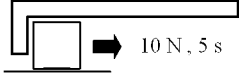
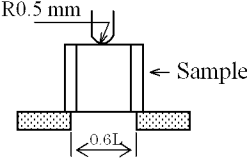
	Item	Standard	Test method
ELECTRICAL CHARACTERISTICS	Inductance	Refer to Table 1	LCR meter (HP4285A or equivalent) Measuring signal level : 1 V
	Self resonant frequency	Refer to Table 1	Impedance/material analyzer (HP4291A or equivalent)
	DC resistance	Refer to Table 1	DC ohm meter (HIOKI3227 or equivalent)
	Rated current	Refer to Table 1.	The maximum DC value having inductance decrease within 30 % and temperature increase within 40 °C by the application of DC bias.
	Insulation resistance	Not less than $1 \times 10^8 \Omega$.	0.2 mm diameter copper wires were wound around the coils three times and measurements were take after 250 V DC was applied between the wire and the terminals for a period of 30 seconds.
	Over current test	No smoke and no fire.	1.5 times the rated current was applied for a period of 5 minutes.
MECHANICAL CHARACTERISTICS	Resistance to Flexure substrate	No damage.	The test samples shall be soldered to the testing board and by reflow soldering conditions as show in table 6. Apply pressure in the direction of the arrow until bent width reaches 2 mm. (※ Notice 1)  Substrate size : 100×40×1.0 Substrate material : glass epoxy-resin Solder cream thickness : 0.12 Unit : mm
	Adhesion of Terminal electrode	No abnormality.	The test samples shall be soldered to the testing board and by reflow soldering conditions as shown table 6. (※ Notice 1)  Applied force : 10 N to X and Y directions. Duration : 5 s. Solder cream thickness : 0.15 mm (Land size refer to recommended Land Pattern Dimensions of "Precaution")
	Body strength	No damage.	Applied force : 10 N Duration : 10 s 

	Table 3	(5 / 1 1)
	STANDARDS	

ENVIRONMENT TESTS

Item	Standard	Test method															
Resistance to vibration	Inductance change: Within $\pm 10 \%$ No abnormality observed in appearance.	The test samples shall be soldered to testing jig as shown in under table. (※ Notice 1) <table><tr><td>Frequency range</td><td>10~55 Hz</td></tr><tr><td>Overall Amplitude</td><td>1.5 mm (Shall not exceed acceleration 196 m/S²)</td></tr><tr><td>Sweeping Method</td><td>10 to 55 to 10 Hz for 1 min.</td></tr><tr><td>Time</td><td>2 hours each in X, Y, and Z Direction.</td></tr></table>	Frequency range	10~55 Hz	Overall Amplitude	1.5 mm (Shall not exceed acceleration 196 m/S ²)	Sweeping Method	10 to 55 to 10 Hz for 1 min.	Time	2 hours each in X, Y, and Z Direction.							
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Overall Amplitude	1.5 mm (Shall not exceed acceleration 196 m/S ²)																
Sweeping Method	10 to 55 to 10 Hz for 1 min.																
Time	2 hours each in X, Y, and Z Direction.																
Resistance to soldering	Inductance change: Within $\pm 10 \%$ No abnormality observed in appearance.	3 time of reflow oven at 230 °C min for 40 sec max, with peak temperature at 260+0/5 °C for 5 sec max . (※ Notice 1) Substrate thickness : 1.0 mm Substrate material : glass epoxy-resin															
Solderability	At least 90 % of terminal electrode is covered by new solder.	The test samples be submerged molten solder as shown in under table. Flux : methanol solution with 25 % of rosin or equivalent. 【Pb free solder : Sn-3Ag-0.5Cu】 <table><tr><td>Solder Temperature</td><td>245±5 °C</td></tr><tr><td>Time</td><td>5±0.5 s</td></tr><tr><td>Immersing Speed</td><td>25 mm/s</td></tr></table> 【Eutectic solder】 <table><tr><td>Solder Temperature</td><td>230±5 °C</td></tr><tr><td>Time</td><td>5±0.5 s</td></tr><tr><td>Immersing Speed</td><td>25 mm/s</td></tr></table>	Solder Temperature	245±5 °C	Time	5±0.5 s	Immersing Speed	25 mm/s	Solder Temperature	230±5 °C	Time	5±0.5 s	Immersing Speed	25 mm/s			
Solder Temperature	245±5 °C																
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Solder Temperature	230±5 °C																
Time	5±0.5 s																
Immersing Speed	25 mm/s																
Temperature characteristics	Inductance change: Within $\pm 15 \%$ No abnormality observed in appearance.	Measurement were taken in a temperature range of −25 °C to +85 °C and the value at +20 °C was used as the standard value.															
Thermal shock	Inductance change: Within $\pm 10 \%$ No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. The test samples shall be left for the specified time at each of temperature in steps from 1 to 4, as shown in under table in sequence. The temperature cycles shall be repeated 100 cycles in the Method. (※ Notice 1) Conditions for 1 cycle. <table><tr><td>Step</td><td>Temperature</td><td>Time (min)</td></tr><tr><td>1</td><td>−40±3 °C</td><td>30±3</td></tr><tr><td>2</td><td>Room Temp.</td><td>within 3</td></tr><tr><td>3</td><td>85±2 °C</td><td>30±3</td></tr><tr><td>4</td><td>Room Temp</td><td>within 3</td></tr></table>	Step	Temperature	Time (min)	1	−40±3 °C	30±3	2	Room Temp.	within 3	3	85±2 °C	30±3	4	Room Temp	within 3
Step	Temperature	Time (min)															
1	−40±3 °C	30±3															
2	Room Temp.	within 3															
3	85±2 °C	30±3															
4	Room Temp	within 3															

	Table 3	(6 / 11)
	STANDARDS	

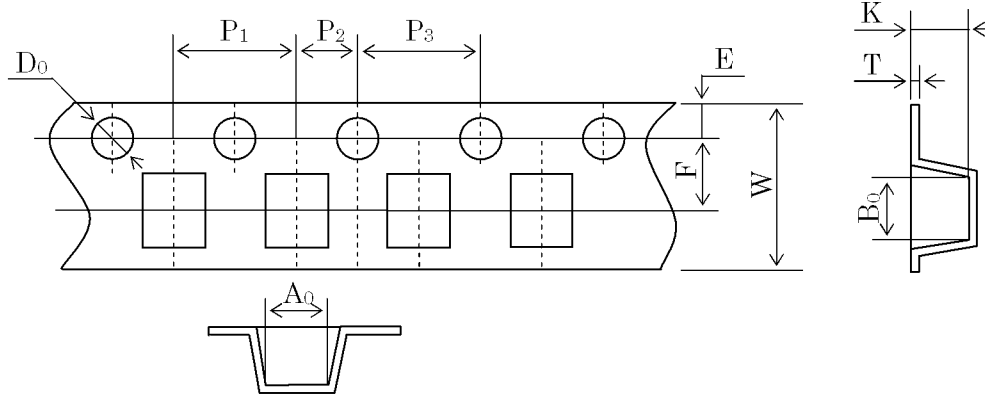
	Item	Standard	Test method							
ENVIRONMENT TESTS	Low temperature life test	Inductance change: Within $\pm 10\%$ No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. And after that proceed the test as shown condition under table. (※ Notice 1) <table><tr><td>Temperature</td><td>$-40\pm 2\text{ }^{\circ}\text{C}$</td></tr><tr><td>Time</td><td>1 000 + 24 hrs</td></tr></table>	Temperature	$-40\pm 2\text{ }^{\circ}\text{C}$	Time	1 000 + 24 hrs			
	Temperature	$-40\pm 2\text{ }^{\circ}\text{C}$								
	Time	1 000 + 24 hrs								
	High temperature life test	Inductance change: Within $\pm 10\%$ No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. And after that proceed the test as shown condition under table. (※ Notice 1) <table><tr><td>Temperature</td><td>$85\pm 2\text{ }^{\circ}\text{C}$</td></tr><tr><td>Time</td><td>1 000 + 24 hrs</td></tr></table>	Temperature	$85\pm 2\text{ }^{\circ}\text{C}$	Time	1 000 + 24 hrs			
Temperature	$85\pm 2\text{ }^{\circ}\text{C}$									
Time	1 000 + 24 hrs									
Damp heat life test	Inductance change: Within $\pm 10\%$ No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. The test samples shall be put in thermostatic oven set at temperature with humidity, as shown in under table. (※ Notice 1) <table><tr><td>Temperature</td><td>$60\pm 2\text{ }^{\circ}\text{C}$</td></tr><tr><td>Humidity</td><td>90~95 %RH</td></tr><tr><td>Time</td><td>1 000 + 24 hrs</td></tr></table>	Temperature	$60\pm 2\text{ }^{\circ}\text{C}$	Humidity	90~95 %RH	Time	1 000 + 24 hrs		
Temperature	$60\pm 2\text{ }^{\circ}\text{C}$									
Humidity	90~95 %RH									
Time	1 000 + 24 hrs									
Loading under damp heat life test	Inductance change: Within $\pm 10\%$ No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. The test samples shall be put in thermostatic oven set at temperature with humidity, as shown in under table, and with the rated current continuously applied. (※ Notice 1) <table><tr><td>Temperature</td><td>$60\pm 2\text{ }^{\circ}\text{C}$</td></tr><tr><td>Humidity</td><td>90~95 %RH</td></tr><tr><td>Current</td><td>Refer to Table 1</td></tr><tr><td>Time</td><td>1 000 + 24 hrs</td></tr></table>	Temperature	$60\pm 2\text{ }^{\circ}\text{C}$	Humidity	90~95 %RH	Current	Refer to Table 1	Time	1 000 + 24 hrs
Temperature	$60\pm 2\text{ }^{\circ}\text{C}$									
Humidity	90~95 %RH									
Current	Refer to Table 1									
Time	1 000 + 24 hrs									

(※ Notice 1) The reliability test that use cream solder eutectic mixture solder.

Standard measuring condition	Unless otherwise specified, measurements were taken within 48 hours after the coils was stored at room temperature and in normal humidity for not less than 2 hour.
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	Table 4	(7 / 1 1)
	TAPING DIMENSIONS	

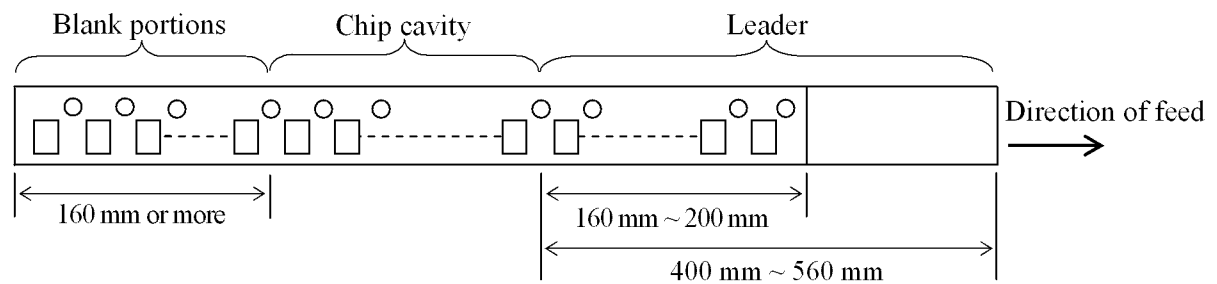
1. Dimensions



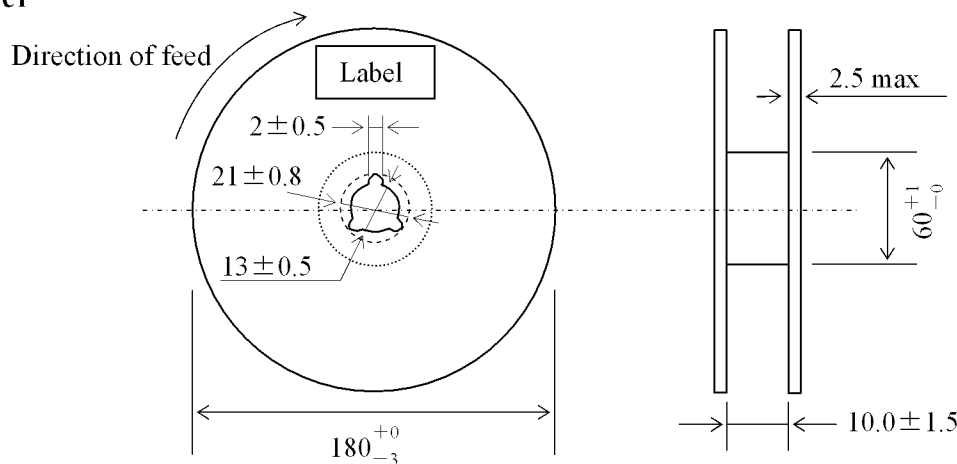
Unit : mm

A_0	B_0	W	F	E	P_1	P_2	P_3	D_0	T	K
2.30 ± 0.20	2.80 ± 0.20	8.00 ± 0.20	3.50 ± 0.05	1.75 ± 0.10	4.00 ± 0.10	2.00 ± 0.10	4.00 ± 0.10	$\phi 1.50$ $+0.10$ -0	0.30 ± 0.05	1.8max

2. Direction of rolling



3. Reel



Unit : mm

Label position : the opposite side of pilot holes

4. Top tape strength



Peel-off strength : 0.2 N ~ 0.7 N
 Peel-off angle : 165° ~ 180°
 Peel-off speed : 300 mm/min

	Table 5	(8 / 1 1)
	PACKING FORM	

1. The number of components

Each reel shall accommodate 3,000 inductors whether there are empty compartments or not.

2. Packing in tape

Emboss carrier tapes of 8 mm width, 4 mm pitch and $\phi 180$ mm-reels shall be used.

3. The allowable number of empty components

The number of empty compartments in a reel, which shall not appear continuously, must be limited to 2.

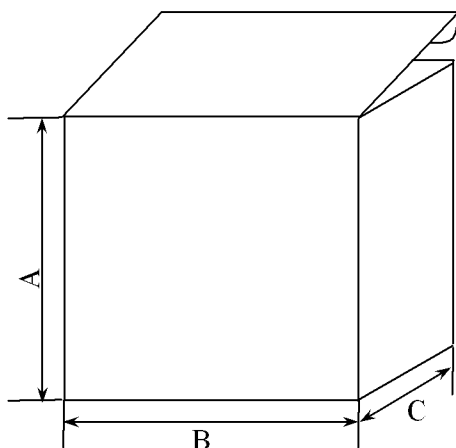
4. Marking

The following items shall be marked legibly each unit pack.

- (1) Customer parts No.
- (2) Our parts No.
- (3) Manufacture's name (TAIYO YUDEN CO., LTD.)
- (4) Control No. (Control number enables to trace shipped lots.)
- (5) Date (stamp)
- (6) Quantity
- (7) Country of the origin

5. Dimensions of packing box (taping)

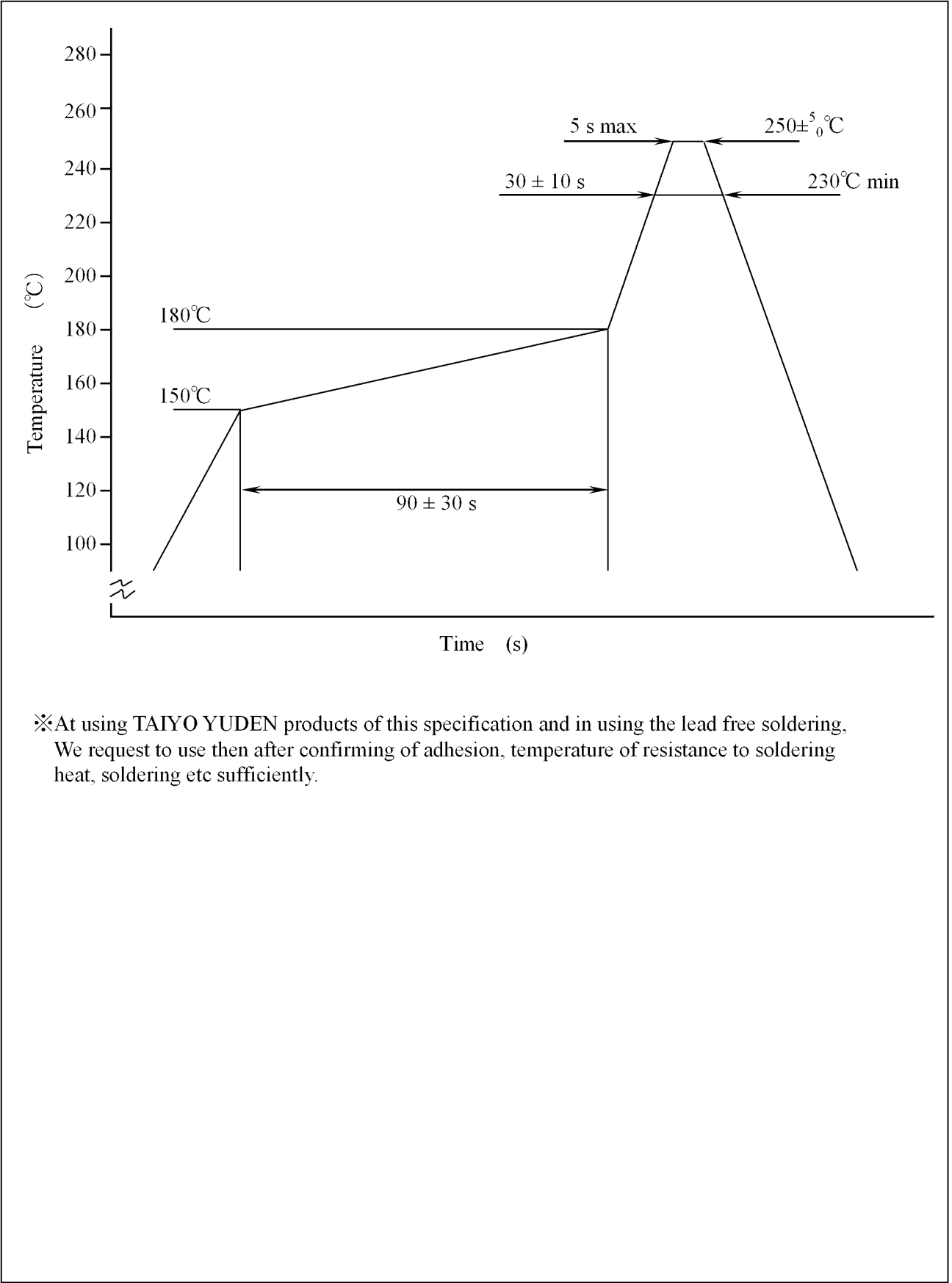
※ Reference



Code	A	B	C	Standard Quantity
Size	190	185	75	15, 000 pcs. max
			140	30, 000 pcs. max

[Unit : mm]

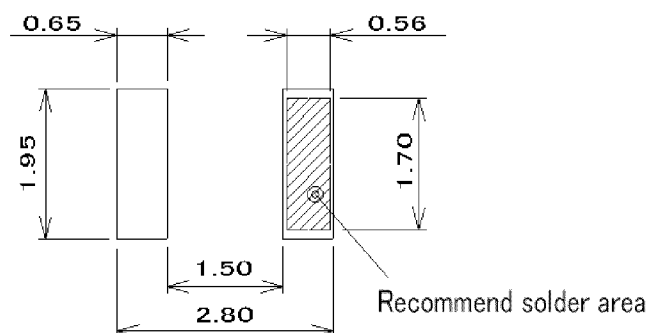
	Table 6	(9 / 1 1)
	REFLOW PROFILE CHART (REFERENCE)	



	Precautions	(10/11)
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1. Surface Mounting

- Mounting and soldering conditions should be checked beforehand.
- This inductors only using reflow soldering.
- Recommended Land-Pattern and Metal Mask-Pattern ($t = 0.10 \sim 0.12$) :



Unit : mm

2. Handling

- Keep the inductors away from all magnets and magnetic objects.
- When splitting the PC boards after mounting inductors, care should be taken not to give any stresses of deflection or twisting to the board.
- Board separation should not be done manually, but by using the appropriate devices.
- Please do not give the inductors any excessive mechanical shocks.
- Please avoid operation, which apply excessive stress and/or temperature to the products, such as resin molding.
- Washing by supersonic waves shall be avoided.

3. Storage

- To maintain the solder ability of terminal electrodes and to keep the packing material in good condition, temperature and humidity in the storage area should be controlled.

Recommended conditions.

Ambient temperature 0 ~ 40 deg

Humidity Below 70 % RH

The ambient temperature must be kept below 30 deg. Even under ideal storage conditions, solder ability of products electrodes may decrease as time passes, so inductors should be used within 6 months from the time of delivery.

4. Regulations

- No ozone-depleting substances, which are defined as Class- I and Class-II in the US Federal Clean Air Act, are used in the production processes, nor contained in the product.
- The product and the specifications described above are not included in the list of export regulations in Japan and USA.
- The product and the specifications described above are conform to "RoHS compliance". "RoHS compliance" means that the product does not contain lead, cadmium, mercury, hexavalent chromium, PBBs or PBDEs referring to EU Directive 2002/95/EC, except other non-restricted substances or impurities which could not be technically removed at the refining process.

5. Production Sites

- TAIYO YUDEN CO., LTD. (JAPAN)
- CHUKI SEIKI CO., LTD. (JAPAN)
- Tsukiyono Denshi Co., Ltd. (JAPAN)

SPECIAL NOTICE

- All of the contents specified here are subject to change without notice due to technical improvements, etc. Therefore, please check latest version of the components specifications carefully before practical application or usage of the components. Please note that Taiyo Yuden Co., Ltd. shall not be responsible for any of deficiency to components or equipments to be used, which are caused under the condition other than specified in the specification.
- This product is developed, designed and intended for use in general electronics equipments. (for AV, household, office supply, information service, telecommunications, etc.). Before incorporating the components into any equipments in the field such as aerospace, aviation, nuclear control, submarine, transportation, (automotive driving and control, passenger protection, train control, ship control), transportation signal, disaster prevention, medical, public information network etc. where higher safety and reliability are especially required, please contact Taiyo Yuden Co., Ltd. for more detail in advance.
And before incorporating the components or devices into the equipments not mentioned in the above, if there is possibility of direct damage or injury to human body, please contact Taiyo Yuden Co., Ltd. for more detail in advance.
In addition, even electronic components or devices are used for the general electronic equipments, if the equipments or the electric circuit require high safety or reliability function or performances, sufficient reliability evaluation-check for the safety shall be performed before use and moreover, due consideration to install a protective circuit is strongly recommended at the design stage.

- ◎ This English version of the specification is made out by translating the Japanese original into English faithfully, but in case where there exists any inconsistency or difference between the two the Japanese original shall govern.