<u>SPECIFICATION</u>

WOUND CHIP INDUCTORS	
CBL TYPE	

TAIYO YUDEN

Specifications

CBL TYPE

(1/11)

1. Range of application

This specification sheet applies to small wound chip inductor, CBL TYPE.

2. Ordering code

Example:
$$\frac{CBL}{(1)} = \frac{2012}{(2)} = \frac{T}{(3)} = \frac{100}{(4)} = \frac{M}{(5)}$$

- (1) Type
- (2) External dimensions
- (3) Packing style (T: Taping)
- (4) Inductance
- (5) Inductance tolerance $(M=\pm 20 \%)$

3. Standard measuring method

Inductance : LCR meter (HP 4285A or equivalent)
Self-resonance frequency : Impedance/Material Analyzer
DC resistance : DC Ohmmeter (A&D AD-5812 or equivalent)

Standard test conditions

Unless otherwise specified, temperature is $20\pm15\,^{\circ}\mathrm{C}$ and the humidity is $65\pm20\,^{\circ}\mathrm{M}$. Should any doubt arise about the test results, further test shall be conducted at a temperature of $20\pm2\,^{\circ}\mathrm{C}$ and a humidity of $65\pm5\,^{\circ}\mathrm{M}$. Inductance and Q are in accordance with our standard measurement figures.

4. Operating temperature range

$$-25$$
 °C to $+105$ °C (Containing self temperature increase)

5. Storage temperature range

$$-40$$
 °C to $+85$ °C (Product without taping)

6. Electrical characteristics

Refer to table 1 and 3.

7. External dimensions and structural diagram

Refer to Table 2.

8. Mechanical characteristics

Refer to Table 3.

9. Environment test performance standards

Refer to Table 3.

10. Taping method

Refer to Table 4.

11. Packing form

Refer to Table 5.

12. Reflow profile chart

Refer to Table 6.

Table 1 ELECTRICAL CHARACTERISTICS (2/11)

Ordering Code	Nominal Inductance	Inductance Tolerance	D.C. Resistance	Self Resonant Frequency	Rated Current		Measuring Frequency
			$\pm 30\%$	min	[mA]	max	
	[µ H]	[%]	$[\Omega]$	[MHz]	1	2	[kHz]
CBL2012T1R0M	1.0	±20	0.15	100	620	950	100
CBL2012T2R2M	2.2	±20	0.39	80	440	590	100
CBL2012T4R7M	4.7	±20	0.66	45	275	490	100
CBL 2012T100M	10	±20	1.0	32	205	370	100
CBL 2012T220M	22	±20	2.1	23	150	250	100
CBL 2012T470M	47	±20	4.2	11	100	140	100

^{*}For rated current of ordinary small power choke coils, please refer to the rated current ① in the above table.

Rated current ① : Current value to guarantee –30 % of nominal inductance

Rated current @: Current value to guarantee component temperature within Δ T=40 $^{\circ}$ C with current flow. (It is not the current to guarantee the inductance value.)

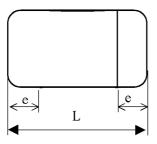
^{*}Rated current ② is the current for instantaneous flow such as plunging current of DC/DC converter. In case of usage in the circuit where large current may be semicontinuously applied over 5 minutes with auto recovery circuit, etc., please contact our sales section before practical application.

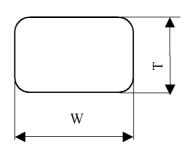
Table 2

EXTERNAL DIMENSIONS AND STRUCTURAL DIAGRAM

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1. External dimensions

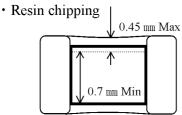




L	2.0 ± 0.2
W	1.25 ± 0.2
Т	0.9 ± 0.1
e	0.5 ± 0.2

Unit: mm

External appearance



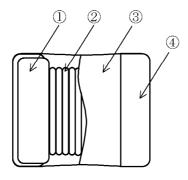
Set \square area as reference for flat level, over 70 % shall be flat. Judgment shall be made according to the dimension below. Insufficient resin on one side shall be 0.45 mm Max. In case of insufficient resin on both side over 0.7 mm of flat surface shall be ensured.

un to 0.5 mm within specified area shall be regarded as non-defec

(Template R part is not included on reference area.)

• The pinhole with ϕ up to 0.5 mm within specified area shall be regarded as non-defective. Resin crack or pinhole which locates outside of specified area or which has contact with frame, shall be also regarded as non-defective.

2. Structural diagram



- ① Ferrite core
- 2 Coil material
- ③ Over-coating resin
- (4) Electrode

Ni – Zn ferrite

Polyurethane-copper wire

Epoxy resin, containing ferrite powder

External electrode (foundation) Ag

External electrode (plating) Ni/Sn

Table 3 STANDARDS (4/11)

	Item	Standard	Test method
	Inductance	Refer to Table 1	LCR meter (HP4285A or equivalent)
STICS	Self resonant frequency	Refer to Table 1	Impedance/material analyzer (HP4291A or equivalent)
CTERI	DC resistance	Refer to Table 1	DC ohm meter (A&D AD-5812 or equivalent)
CHARACTERISTIC	Rated current	Refer to Table 1.	The maximum DC value having inductance decrease within 30 % and temperature increase within 40 °C by the application of DC bias shall be respectively measured.
ELECTRICAL	Insulation resistance	Not less then $1 \times 10^8 \Omega$.	0.2 mm diameter copper wires were wound around the coils three times and measurements were take after 50 V DC was applied between the wire and the terminals for a period of 30 seconds.
EL	Over current test	No smoke and no fire.	1.5 times the rated current ② was applied for a period of 5 minutes.
RACTERISTICS	Resistance to Flexure substrate	No damage.	The test samples shall be soldered to the testing board and by reflow soldering conditions as show in table 6. Apply pressure in the direction of the arrow until bent width reaches 2 mm. Pressure R30 R30 R00 R230 R30 R00 R30 R30 R30 R30 R30 R30 R30 R
MECHANICAL CHAR	Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by reflow soldering conditions as shown table 6. Applied force: 10 N to X and Y directions. Duration: 5 s. Solder cream thickness: 0.15 mm (Land size refer to recommended Land Pattern Dimensions of "Precaution")
	Body strength	No damage.	Applied force: 10 N Duration: 10 s R0.5 mm C Sample

Table 3	
STANDARDS	(5/11)

	Item	Standard	Test method			
	Resistance to vibration	\triangle L/L \rightarrow within \pm 10 % No abnormality observed in appearance.	The test samples shall be soldered to testing jig as shown in under table. Frequency range 10~55 Hz Overall Amplitude 1.5 mm (Shall not exceed acceleration 196 m/S²)			
			Sweeping Method 10 to 55 to 10 Hz for 1 min. Time 2 hours each in X, Y, and Z Direction.			
	Resistance to soldering	\triangle L/L \rightarrow within \pm 10 % No abnormality observed in appearance.	3 time of reflow oven at 230 °C min for 30 ± 10 sec, with peak temperature at $250\pm\frac{5}{0}$ °C for 5 sec max.			
			Substrate thickness: 1.0 mm Substrate material: glass epoxy-resin			
TESTS	Solderability	At least 90 % of terminal electrode is covered by new solder.	The test samples be submerged molten solder as shown in under table. Flux: methanol solution with 25 % of rosin or equivalent.			
ENVIRONMENT	Temperature characteristics	△L/L → within±15 % No abnormality observed in appearance.	Measurement were taken in a temperature range of $-25~^{\circ}\text{C}$ to $+85~^{\circ}\text{C}$ and the value at $+20~^{\circ}\text{C}$ was used as the standard value.			
ENVIR	Thermal shock	△L/L → within±10 % No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. The test samples shall be left for the specified time at each of temperature in steps from 1 to 4, as shown in under table in sequence. The temperature cycles shall be repeated 100 cycles in the Method. Conditions for 1 cycle. Step Temperature Time (min) 1 -40±3 °C 30±3 2 Room Temp. within 3 3 85±2 °C 30±3 4 Room Temp within 3			
	Low temperature life test	\triangle L/L \rightarrow within \pm 10 % No abnormality observed in appearance.	5 J-5			

Table 3	
STANDARDS	(6/11)

	Item	Standard	Test method
	High temperature life test	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. And after that proceed the test as shown condition under table.	
ENVIRONMENT TESTS	Damp heat life test	△L/L → within±10 % No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. The test samples shall be put in thermostatic oven set at temperature with humidity, as shown in under table.
ENVI	Loading under damp heat life test	\triangle L/L \rightarrow within \pm 10 % No abnormality observed in appearance.	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in table 6. The test samples shall be put in thermostatic oven set at temperature with humidity, as shown in under table, and with the rated current ① continuously applied.

Standard measuring condition	Unless otherwise specified, measurements were taken within 48 hours after the coils was stored at room temperature and in normal humidity for not less than 2 hour.
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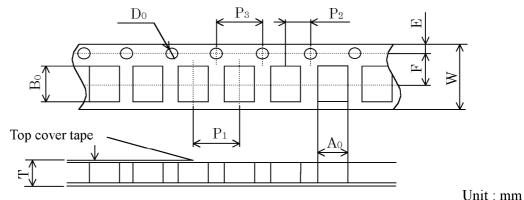
- Class- I and Class- II ozone depleting substances (ODS), etc., which are regulated by the Federal Law for Atmosphere Purification, are not included in the products not it applied to the products at any stage of manufacturing processes.
- The bromide flame proof materials are not used at all.
 The product and the specifications described above are not included in the list of export regulations in Japan and USA.

Table 4

TAPING DIMENSIONS

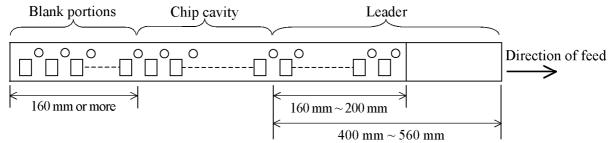
(7/11)

1. Dimensions

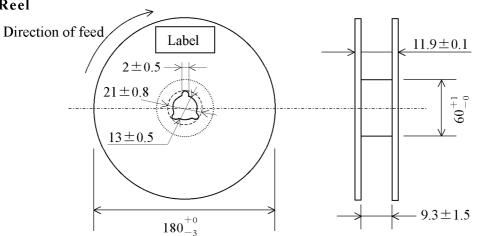


\mathbf{A}_0	\mathbf{B}_0	W	F	E	\mathbf{P}_1	\mathbf{P}_2	\mathbf{P}_3	\mathbf{D}_0	T
1.55 ±0.20	2.30 ± 0.20	8.00 ± 0.30	3.50 ± 0.05	1.75 ± 0.10	4.00 ± 0.20	2.00 ± 0.05	$\begin{array}{ c c c } 4.00 \\ \pm 0.10 \end{array}$	φ 1.50 +0.10 -0	1.1 Max

2. Direction of rolling

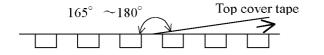


3. Reel



Label position: the opposite side of pilot holes

4. Top tape strength



Peel-off strength : $0.15 \text{ N} \sim 0.5 \text{ N}$ Peel-off angle : $165^{\circ} \sim 180^{\circ}$ Peel-off speed : 300 mm/min

Unit: mm

Table 5	
PACKING FORM	(8/11)

1. The number of components

Each reel shall accommodate 4000 inductors whether there are empty compartments or not.

2. Packing in tape

Emboss carrier tapes of 8 mm width, 4 mm pitch and ϕ 180 mm-reels shall be used.

3. The allowable number of empty components

The number of empty compartments in a reel, which shall not appear continuously, must be limited to 2.

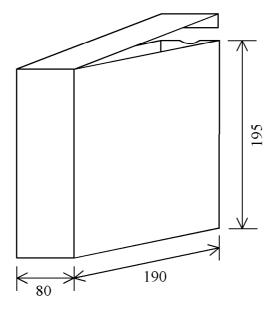
4. Marking

The following items shall be marked legibly each unit pack.

- (1) The name of manufacturing company
- (2) Manufacture's name (Taiyo Yuden Co., Ltd.)
- (3) Lot number
- (4) Date (stamp)
- (5) The number of components packed in a reel

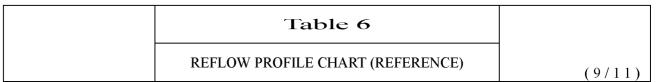
5. Dimensions of packing box (taping)

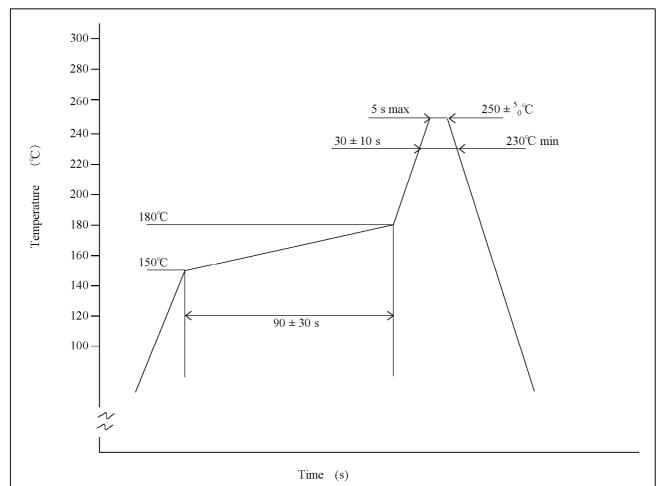
Reference



Standard Quantity 20 000 pcs.

Unit: mm





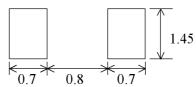
* At using TAIYO YUDEN products of this specification and in using the lead free soldering, We request to use then after confirming of adhesion, temperature of resistance to soldering heat, soldering etc sufficiently.

Precautions

(10/11)

1. Surface Mounting

- Mounting and soldering conditions should be checked beforehand.
- This inductors only using reflow soldering.
- · Recommended Land-Pattern:



Unit: mm

• Recommended conditions for using a soldering iron:

Put the soldering iron on the land-pattern.

Soldering iron's temperature Below 350 °C

Duration 3 seconds or less

• The soldering iron should not directly touch the inductor.

2. Handling

- Keep the inductors away from all magnets and maganetic objects.
- When splitting the PC boards after mounting inductors, care should be taken not to give any stresses of deflection or twisting to the board.
- Board separation should not be done manually, but by using the appropriate devices.
- Please do not give the inductors any excessive mechanical shocks.
- Please avoid operation, which apply excessive stress and/or temperature to the products, such as resin molding.
- Washing by supersonic waves shall be avoided.

3. Storage

• To maintain the solderability of terminal electrodes and to keep the packing material in good condition, temperature and humidity in the storage area should be controlled.

Recommended conditions.

Ambient temperature

Humidity

ure $0 \sim 40 \,^{\circ}\text{C}$ Below 70 % RH

The ambient temperature must be kept below 30 °C. Even under ideal storage conditions, solderability of products electrodes may decrease as time passes, so inductors should be used within 6 months from the time of delivery.

4. Production Sites

- · TAIYO YUDEN CO., LTD. (JAPAN)
- · CHUKI SEIKI CO., LTD. (JAPAN)
- This English version of the specification is made out by translating the Japanese original into English faithfully, but in case where there exists any inconsistency or difference between the two the Japanese original shall govern.

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