



* Pb Free Part



MSL1

Customer Name	Standard	TAIYO YUDEN Mobile Technology Co.,Ltd.	
System	Band V Duplexer	Date	March 31, 2010
Part Number	FAR-D5NE-881M50-P1A6Q	Version 2.3de	

Table 1-1. Electrical Specification

Item			Condition (MHz)	Specification			Unit	Remarks
				Min	Typ	Max		
Tx to ANT	Insertion loss		824~849	-	1.6	1.9(*2)	dB (*1)	
	VSWR	ANT	824~849	-	1.6	2.0	-	
		Tx		-	1.5	2.0	-	
	Input Power		824~849	+29dBm Ta=+50°C 50000h,CW			-	
	Absolute attenuation		10~420	30	42	-	dB	
			420~494	35	40	-	dB	
			494~804	30	36	-	dB	
			860~869	2	8	-	dB	
			869~894	45	53	-	dB	
			1574~1577	43	50	-	dB	
			1638~1708	35	46	-	dB	
			1844.9~1990	30	39	-	dB	
			2110~2170	27	37	-	dB	
			2400~2557	25	33	-	dB	
			3286~3406	20	31	-	dB	
			4110~4255	5	14	-	dB	
			4934~5350	3	8	-	dB	
			5725~5953	3	12	-	dB	
Ant to Rx	Insertion loss		869~894	-	1.7	2.2(*2)	dB (*1)	
	Phase balance		869~894	-10	-1/+2	+10	deg	
	Amplitude balance		869~894	-1.0	-0.2/+0.1	+1.0	dB	
	VSWR	ANT	869~894	-	1.8	2.1	-	
		Tx		-	1.5	2.0	-	
	Absolute attenuation		0.2~447	45	85	-	dB	
			447~824	30	60	-	dB	
			824~849	50	60	-	dB	
			849~854	10	54	-	dB	
			909~1000	10	18	-	dB	
			1000~1850	35	60	-	dB	
			1850~1920	40	60	-	dB	
			1920~4480	35	46	-	dB	



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Table 1-1. Electrical Specification

Item		Condition (MHz)	Specification			Unit	Remarks
			Min	Typ	Max		
Tx to Rx	Isolation	824~849	55	60	-	dB	Differential rejection
			50	55	-	dB	Common mode rejection
		869.7~893.37	50	53	-	dB	
Terminating Impedance		Tx port	50			Ohm	Single-ended
		Rx port	100			Ohm	Differential
		Ant port	50 // 8.2nH			Ohm	Single-ended
Operating Temperature			-30 to +85			°C	
Device size (L typ. x W typ. x H max.)			2.5 x 2.0 x 0.6			mm	

(*1) Specification of insertion loss excludes loss that comes from the test board. (Approximately 0.05dB)

(*2) Case for parallel inductor (8.2nH, $Q = \infty$) utilized at ANT port of duplexer.



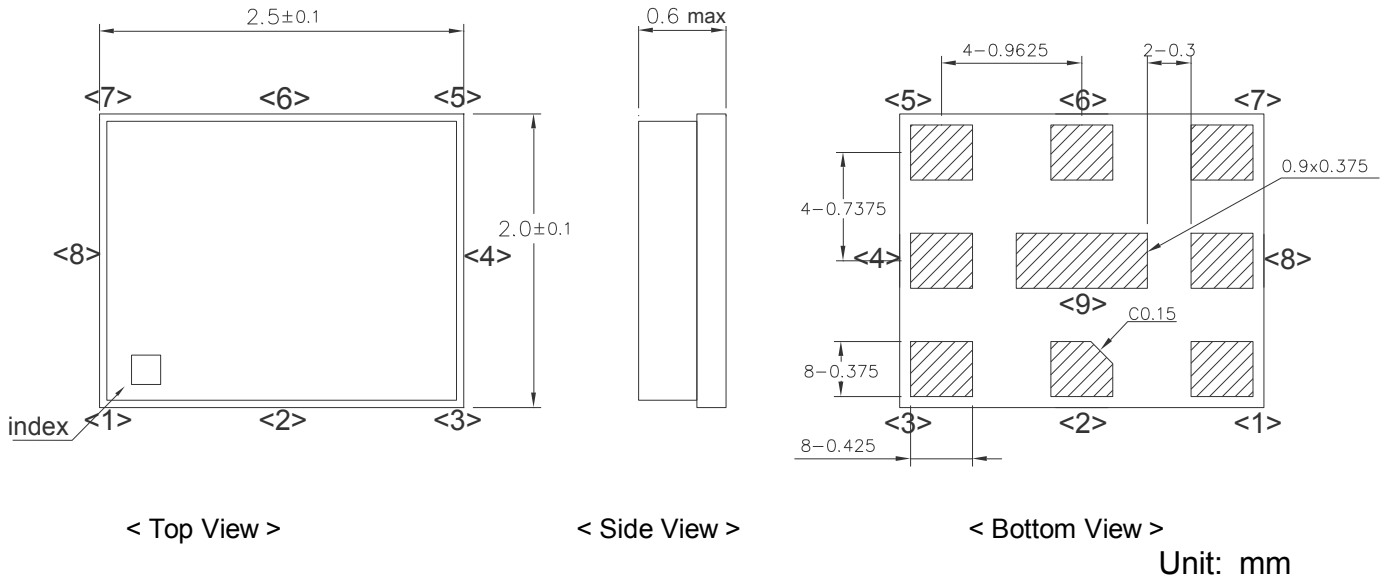
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Dimensions



Pin Configuration

Pin No.	Pin name	Description
1	Rx	Receiver Pin (balanced)
2	GND	Ground Pin
3	Tx	Transmitter Pin
4	GND	Ground Pin
5	GND	Ground Pin
6	ANT	Antenna Pin
7	GND	Ground Pin
8	Rx	Receiver Pin (balanced)
9	GND	Ground Pin

Figure 1. Dimensions and Pin assignment



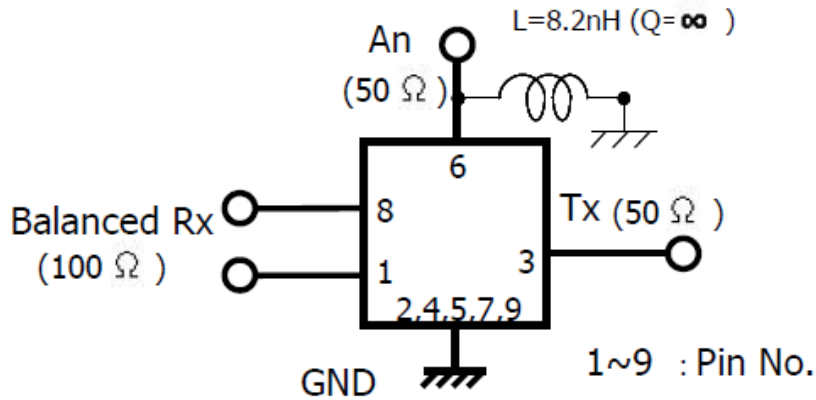
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Evaluation Circuit



Recommended foot print pattern

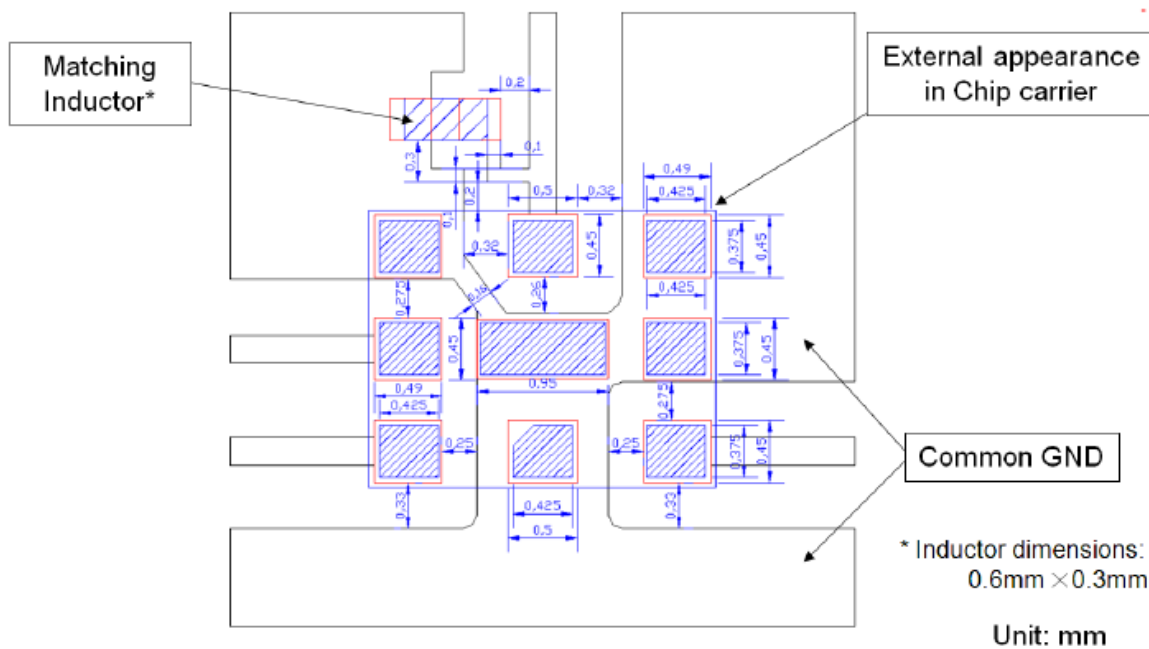


Figure 2. Recommended foot print pattern



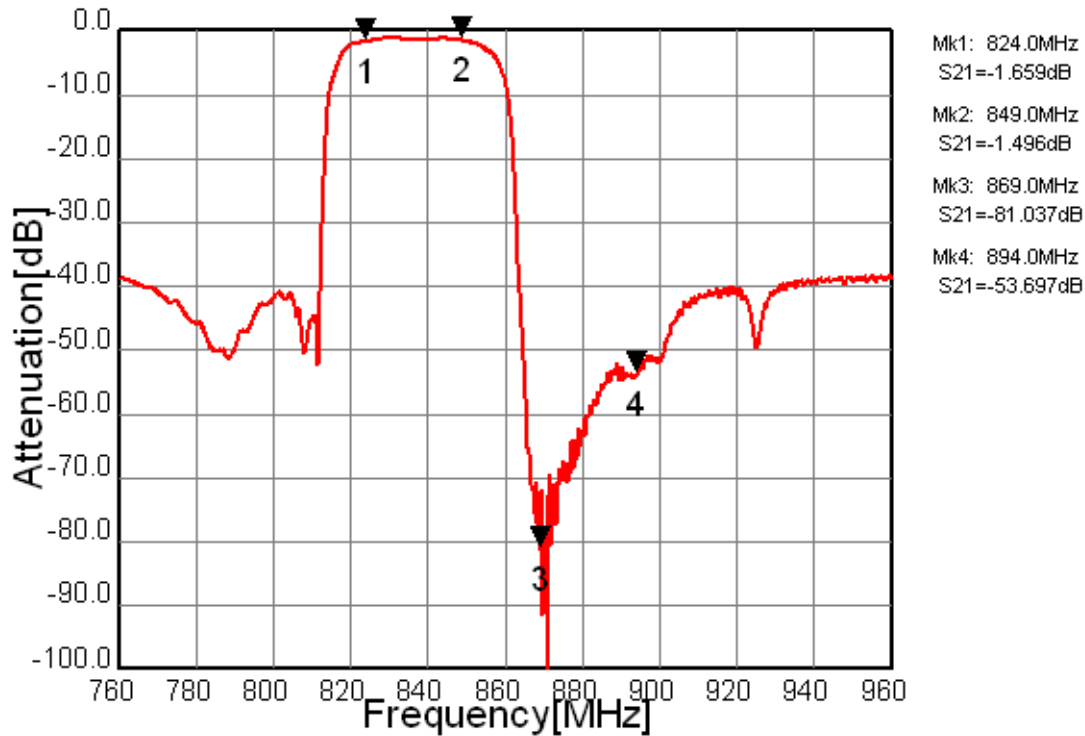
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Tx to Ant



Ant to Rx

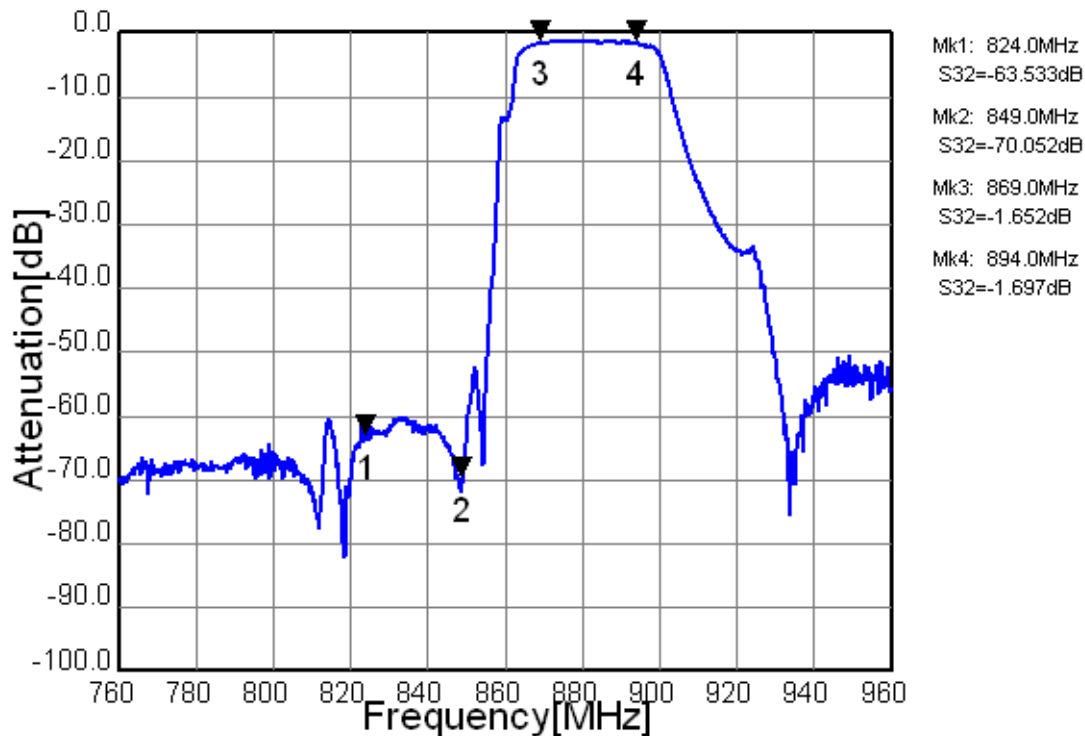


Figure 3-1. Electrical Characteristics

These data **include** loss that comes from the test board. (Approximately 0.05dB)

TAIYO YUDEN



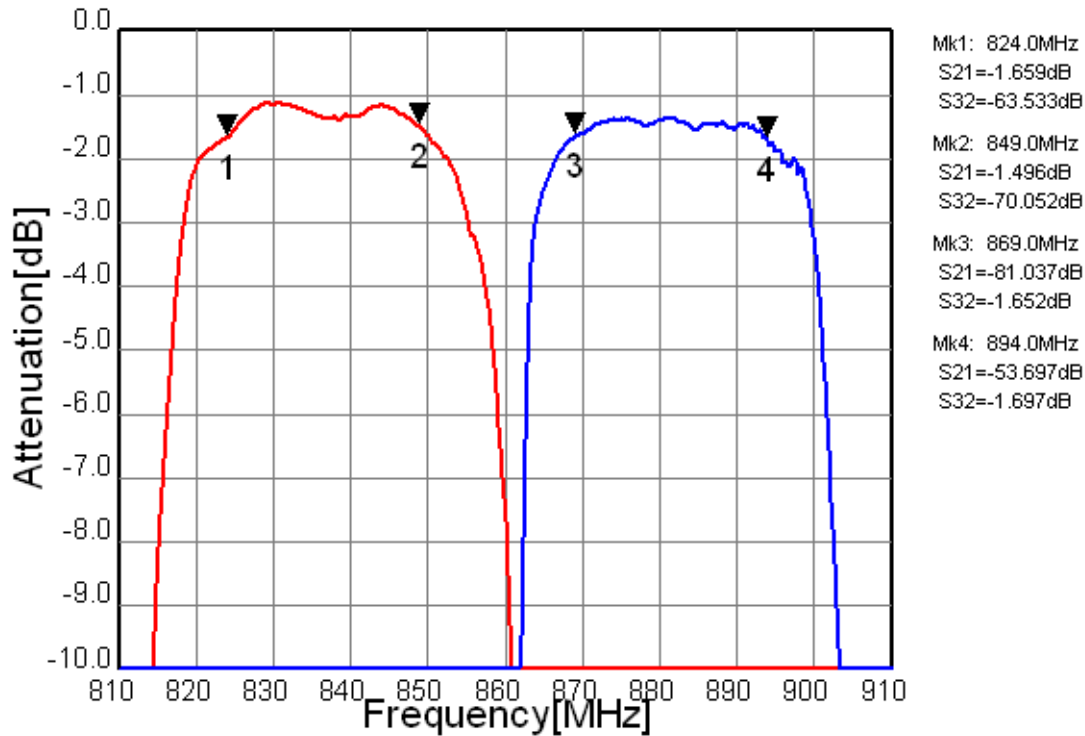
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Tx to Ant, Ant to Rx



Tx to Rx Isolation

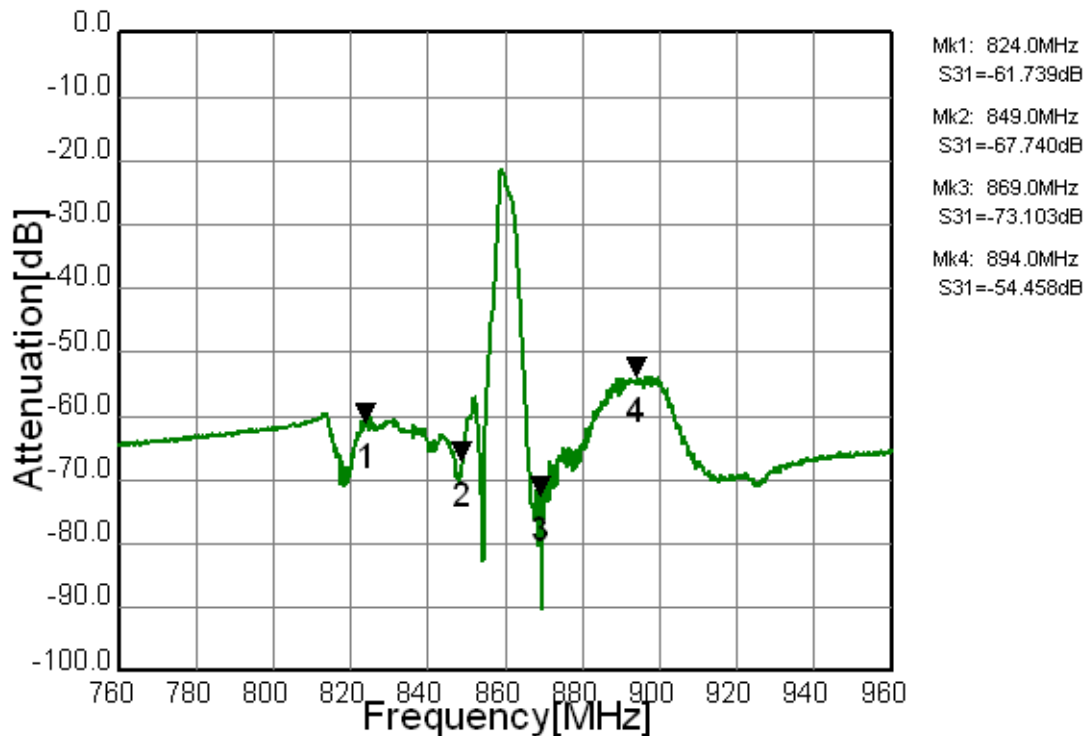


Figure 3-2. Electrical Characteristics

These data **include** loss that comes from the test board. (Approximately 0.05dB)

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Tx Port

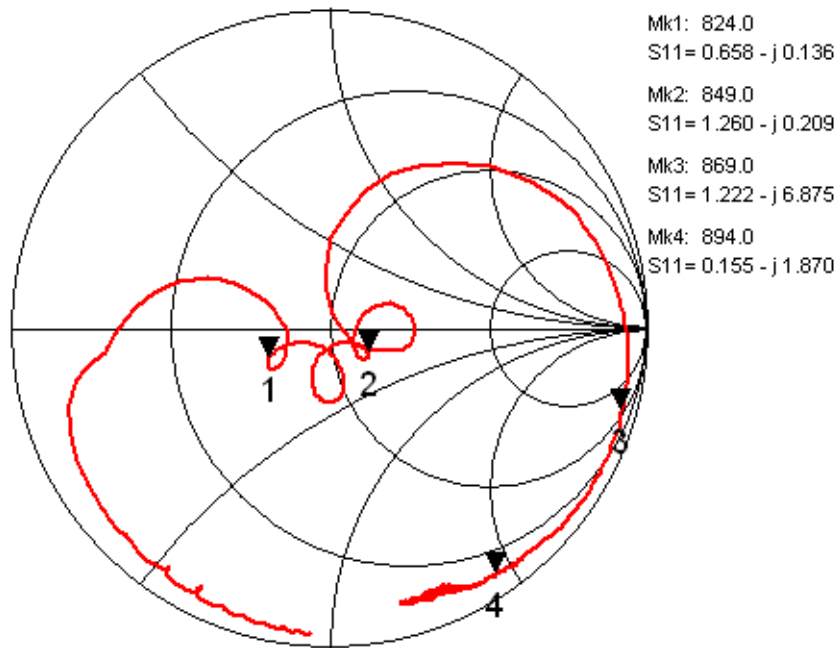
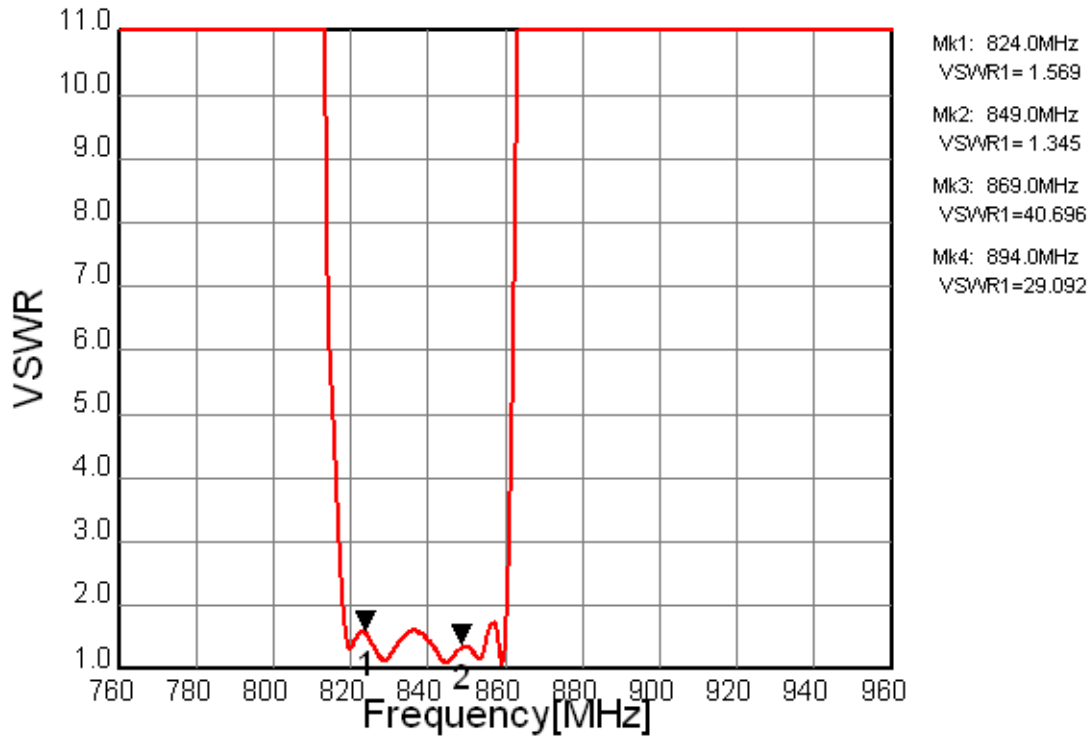


Figure 3-3. Electrical Characteristics



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Rx Port

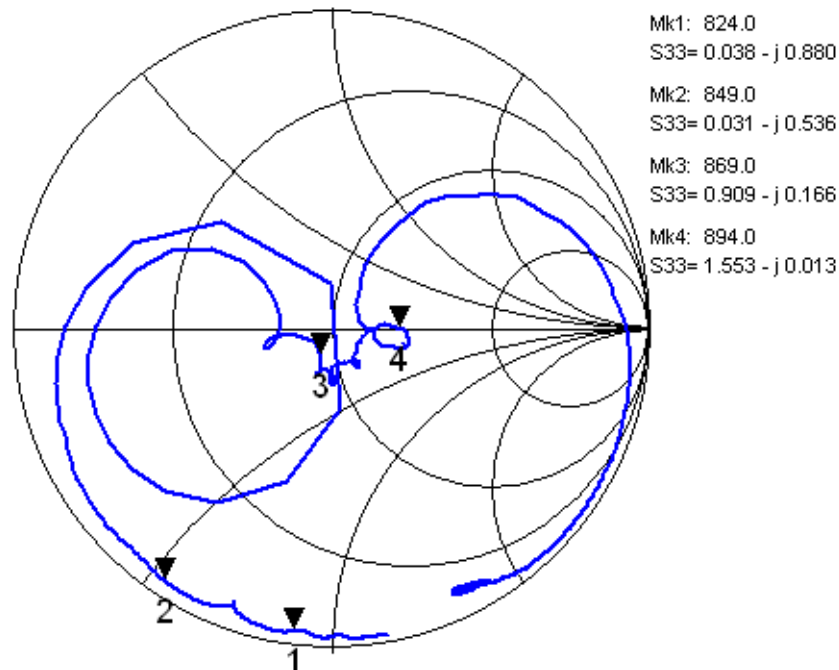
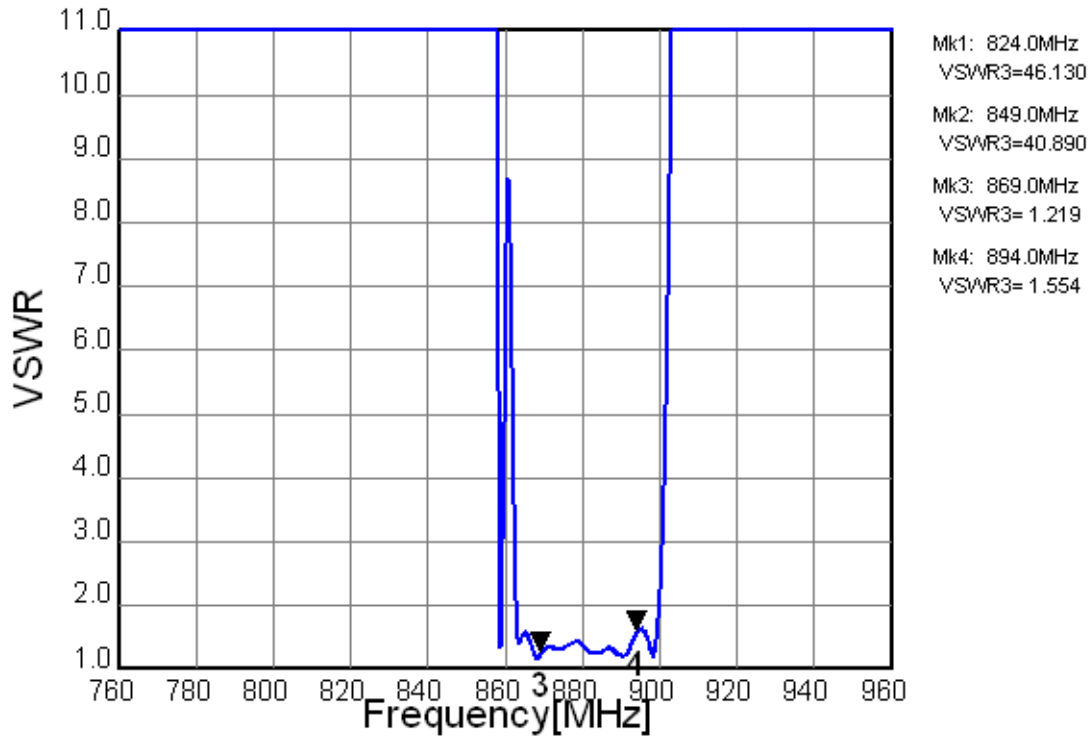


Figure 3-4. Electrical Characteristics



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Ant Port

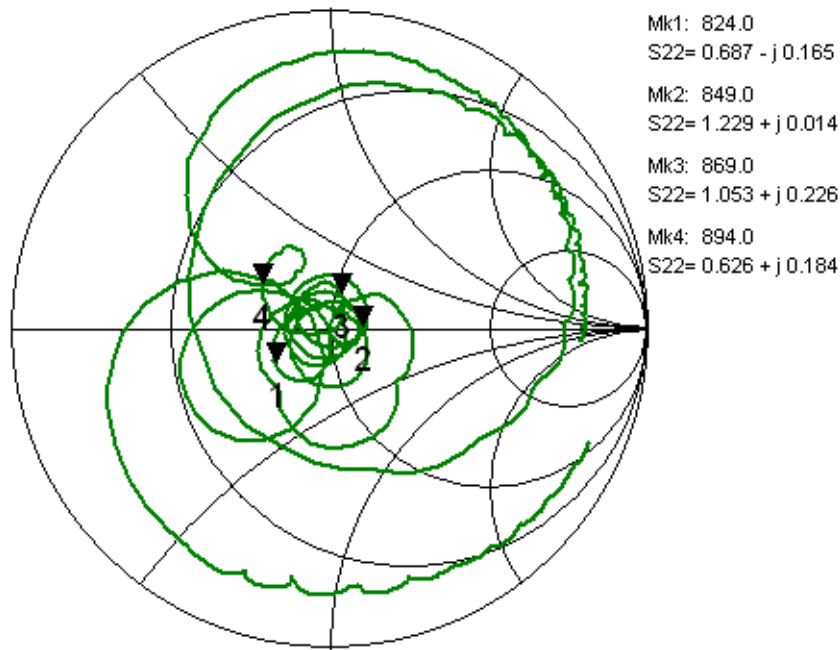
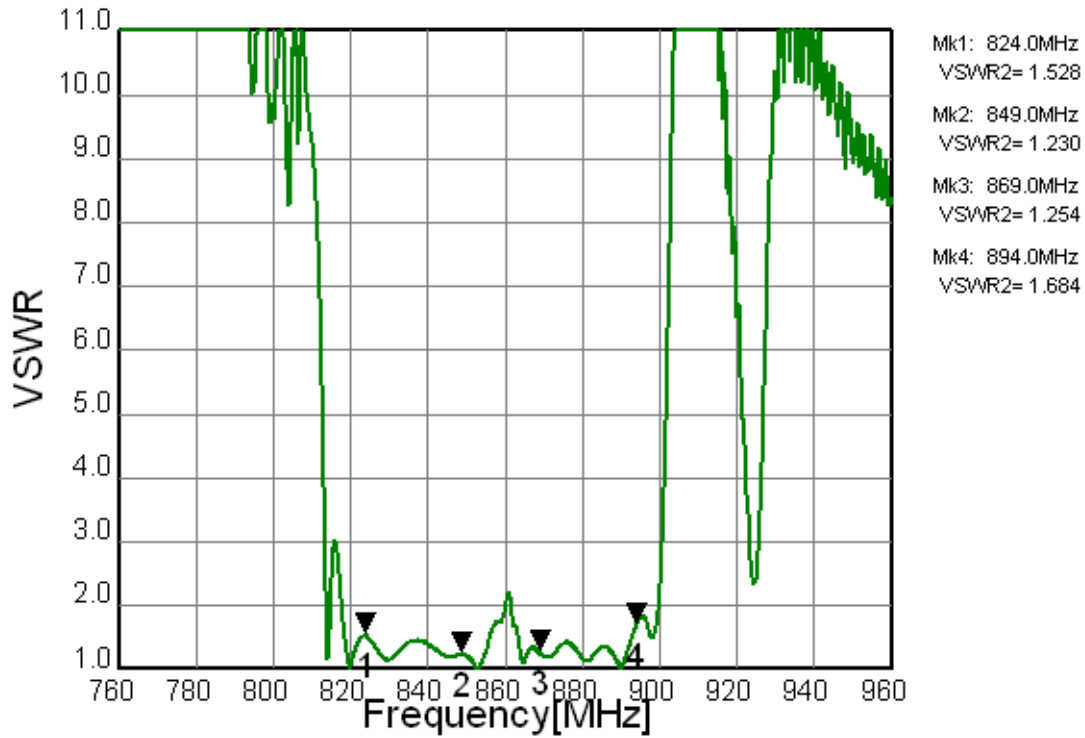


Figure 3-5. Electrical Characteristics



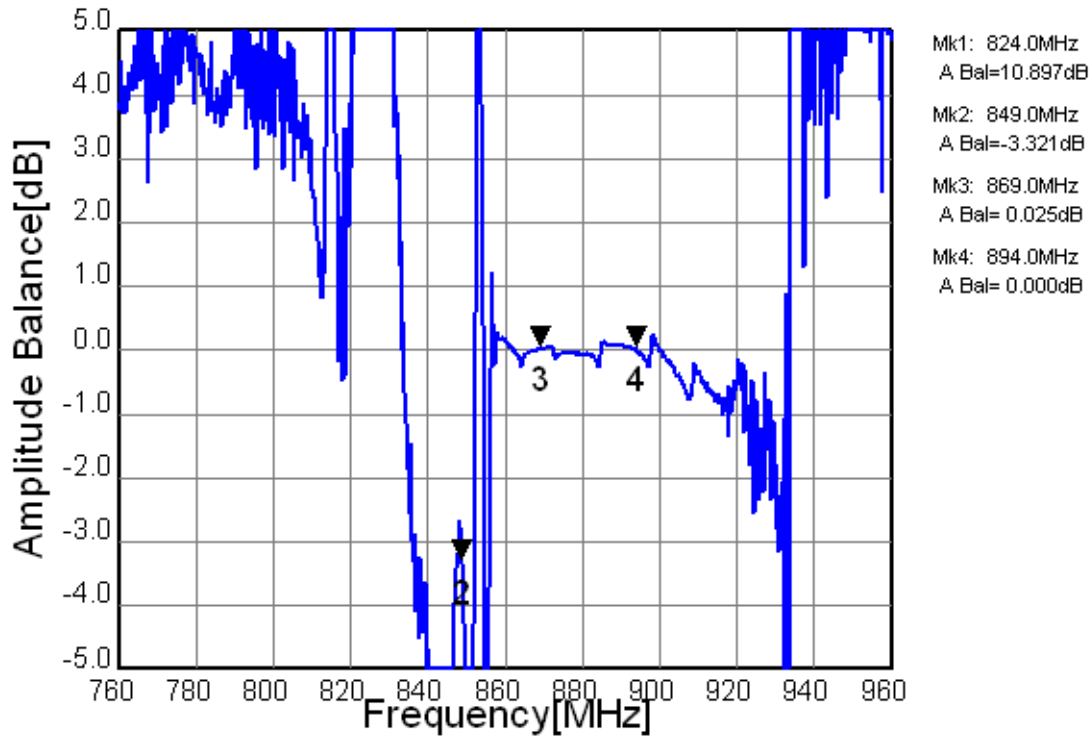
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Ant to Rx (Amplitude balance)



Ant to Rx (Phase balance)

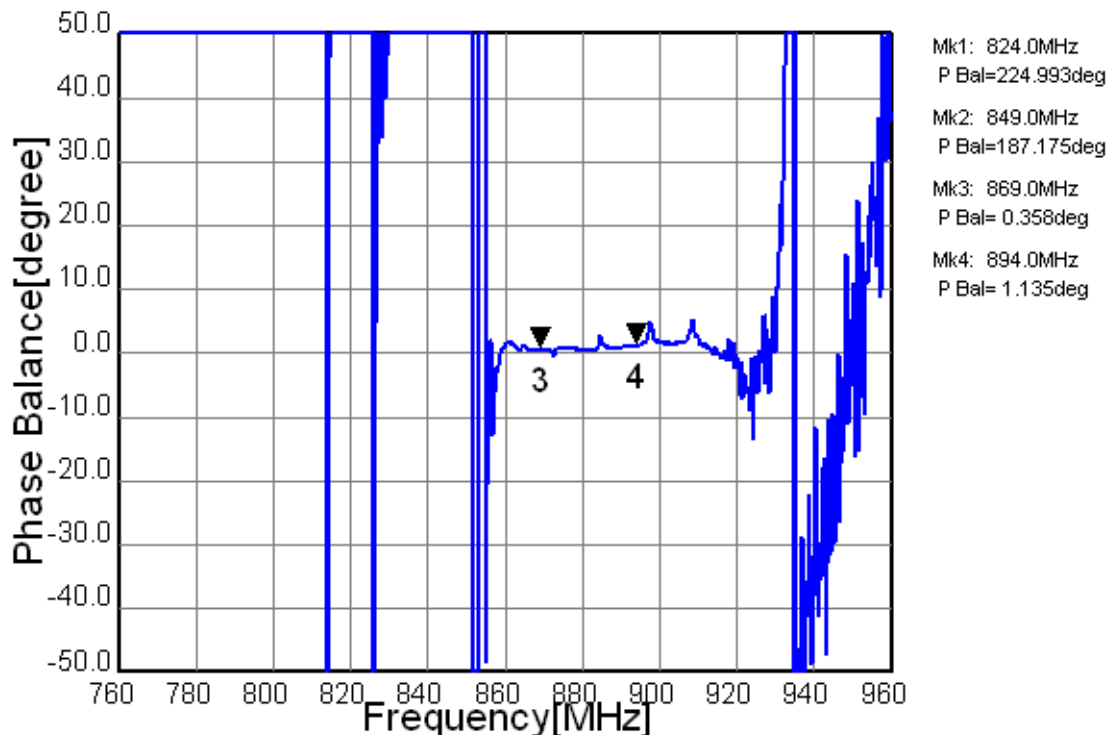


Figure 3-6. Electrical Characteristics



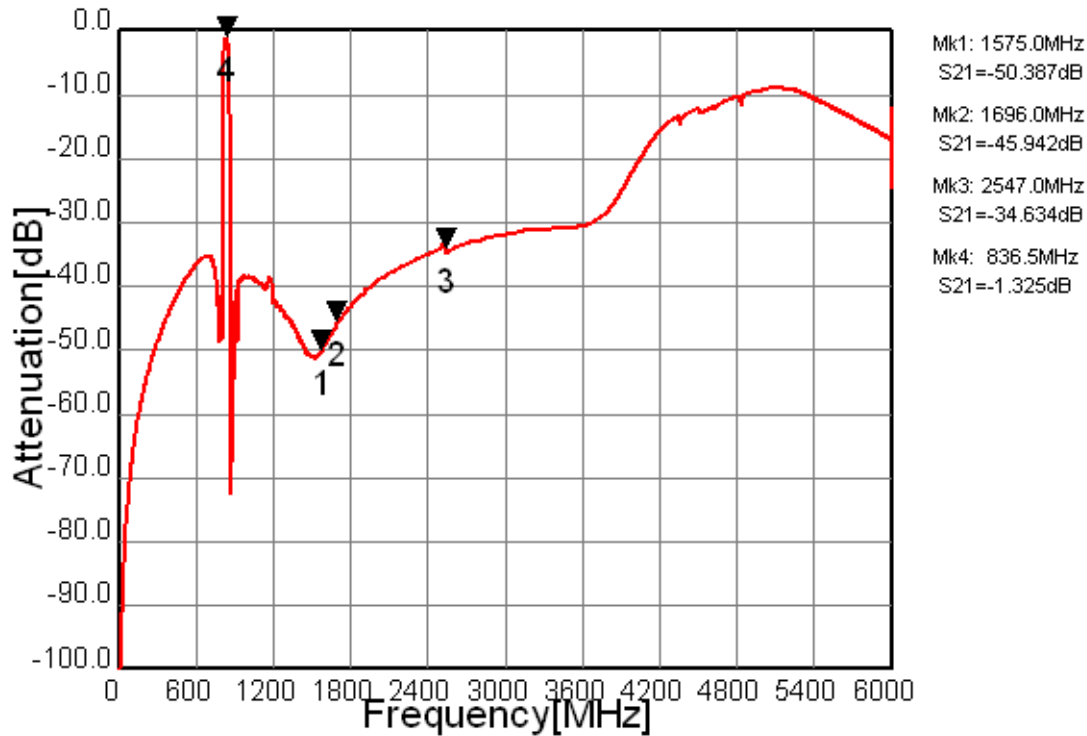
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Tx to Ant (Wide span)



Ant to Rx (Wide span)

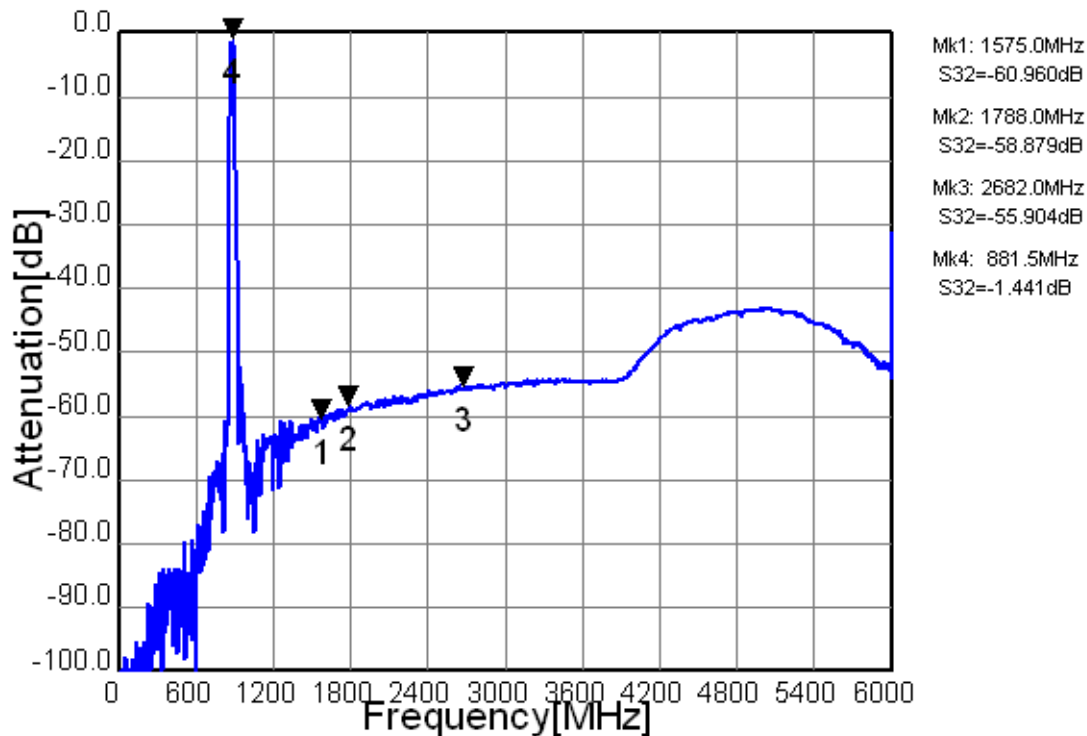


Figure 3-7. Electrical Characteristics